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Electronic Version v1.1

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**Title of
Invention****METHOD FOR MANUFACTURING WAFER LEVEL CHIP SCALE
PACKAGE STRUCTURE**

Application Number :

Date :

First Named Applicant: Mr. Min-Chih Hsuan

Confirmation Number:

Attorney Docket Number: 13301-US-PA

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Documents being submitted:	Files
us-request	13301-usrequ.xml us-request.dtd us-request.xsl
us-assignment	13301-usassn.xml us-assignment.xsl us-assignment.dtd 13301assignment01.tif 13301assignment02.tif
us-fee-sheet	13301-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	13301usf.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif Image5.tif Image6.tif Image7.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent

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